UART Result

[2021-03-25 07:51:20 EDT] xrun -Q -unbuffered '-timescale' '1ns/1ns' '-sysv' '-access' '+rw' design.sv testbench.sv   
TOOL: xrun 20.09-s003: Started on Mar 25, 2021 at 07:51:21 EDT  
xrun: 20.09-s003: (c) Copyright 1995-2020 Cadence Design Systems, Inc.  
 Top level design units:  
 uart\_top\_tb  
xmelab: \*W,DSEMEL: This SystemVerilog design will be simulated as per IEEE 1800-2009 SystemVerilog simulation semantics. Use -disable\_sem2009 option for turning off SV 2009 simulation semantics.  
Loading snapshot worklib.uart\_top\_tb:sv .................... Done  
xmsim: \*W,DSEM2009: This SystemVerilog design is simulated as per IEEE 1800-2009 SystemVerilog simulation semantics. Use -disable\_sem2009 option for turning off SV 2009 simulation semantics.  
xcelium> source /xcelium20.09/tools/xcelium/files/xmsimrc  
xcelium> run  
Simulation complete via $finish(1) at time 960 NS + 0  
./testbench.sv:183 $finish;  
xcelium> exit  
TOOL: xrun 20.09-s003: Exiting on Mar 25, 2021 at 07:51:22 EDT (total: 00:00:01)  
Creating result.zip...  
 adding: uart\_top.vcd (deflated 80%)  
 adding: xcelium.d/ (stored 0%)  
 adding: xcelium.d/history (deflated 18%)  
 adding: xcelium.d/pakvst.ts (stored 0%)  
 adding: xcelium.d/.history.lock (stored 0%)  
 adding: xcelium.d/run.lnx86.20.09.d/ (stored 0%)  
 adding: xcelium.d/run.lnx86.20.09.d/.xmrun.lock (stored 0%)  
 adding: xcelium.d/run.lnx86.20.09.d/UVMHOME (stored 0%)  
 adding: xcelium.d/run.lnx86.20.09.d/bind.lst.lnx86 (deflated 37%)  
 adding: xcelium.d/run.lnx86.20.09.d/xllibs/ (stored 0%)  
 adding: xcelium.d/run.lnx86.20.09.d/OVMHOME (stored 0%)  
 adding: xcelium.d/run.lnx86.20.09.d/temp/ (stored 0%)  
 adding: xcelium.d/run.lnx86.20.09.d/xmsim\_restart.args (deflated 36%)  
 adding: xcelium.d/run.lnx86.20.09.d/.timestamp.ts (deflated 30%)  
 adding: xcelium.d/run.lnx86.20.09.d/cds.lib (stored 0%)  
 adding: xcelium.d/run.lnx86.20.09.d/.ncv.lock (stored 0%)  
 adding: xcelium.d/run.lnx86.20.09.d/xmelab.args (deflated 33%)  
 adding: xcelium.d/run.lnx86.20.09.d/xmvlog.args (deflated 40%)  
 adding: xcelium.d/run.lnx86.20.09.d/xmvlog.env (deflated 40%)  
 adding: xcelium.d/run.lnx86.20.09.d/xrun.args (deflated 17%)  
 adding: xcelium.d/run.lnx86.20.09.d/xmsim.env (deflated 30%)  
 adding: xcelium.d/run.lnx86.20.09.d/xmsim\_restart.env (deflated 30%)  
 adding: xcelium.d/run.lnx86.20.09.d/hdlrun.var (deflated 72%)  
 adding: xcelium.d/run.lnx86.20.09.d/xmvlog.files (deflated 22%)  
 adding: xcelium.d/run.lnx86.20.09.d/xmelab.hrd (deflated 6%)  
 adding: xcelium.d/run.lnx86.20.09.d/.incl2.file (deflated 71%)  
 adding: xcelium.d/run.lnx86.20.09.d/xmelab.env (deflated 40%)  
 adding: xcelium.d/run.lnx86.20.09.d/6427a52e0b86\_13\_cdsrun.lib (deflated 6%)  
 adding: xcelium.d/run.lnx86.20.09.d/cdsrun.lib (deflated 6%)  
 adding: xcelium.d/run.lnx86.20.09.d/6427a52e0b86\_13\_hdlrun.var (deflated 72%)  
 adding: xcelium.d/run.lnx86.20.09.d/.xmlib.lock (stored 0%)  
 adding: xcelium.d/run.lnx86.20.09.d/.xmlibpath (deflated 28%)  
 adding: xcelium.d/run.lnx86.20.09.d/hdl.var (deflated 42%)  
 adding: xcelium.d/run.lnx86.20.09.d/files.ts (deflated 30%)  
 adding: xcelium.d/run.lnx86.20.09.d/xmsim.args (deflated 31%)  
 adding: xcelium.d/worklib/ (stored 0%)  
 adding: xcelium.d/worklib/pakvst.ts (stored 0%)  
 adding: xcelium.d/worklib/xm.lnx86.070.pak (deflated 90%)  
 adding: xcelium.d/worklib/cdsinfo.tag (stored 0%)  
 adding: xcelium.d/worklib/.xm.db.070.lnx86 (deflated 38%)  
 adding: xcelium.d/worklib/.cdsvmod (stored 0%)  
 adding: xcelium.d/run.d/ (stored 0%)  
 adding: xcelium.d/run.d/.xmrun.lock (stored 0%)  
 adding: xcelium.d/run.d/UVMHOME (stored 0%)  
 adding: xcelium.d/run.d/bind.lst.lnx86 (deflated 37%)  
 adding: xcelium.d/run.d/xllibs/ (stored 0%)  
 adding: xcelium.d/run.d/OVMHOME (stored 0%)  
 adding: xcelium.d/run.d/temp/ (stored 0%)  
 adding: xcelium.d/run.d/xmsim\_restart.args (deflated 36%)  
 adding: xcelium.d/run.d/.timestamp.ts (deflated 30%)  
 adding: xcelium.d/run.d/cds.lib (stored 0%)  
 adding: xcelium.d/run.d/.ncv.lock (stored 0%)  
 adding: xcelium.d/run.d/xmelab.args (deflated 33%)  
 adding: xcelium.d/run.d/xmvlog.args (deflated 40%)  
 adding: xcelium.d/run.d/xmvlog.env (deflated 40%)  
 adding: xcelium.d/run.d/xrun.args (deflated 17%)  
 adding: xcelium.d/run.d/xmsim.env (deflated 30%)  
 adding: xcelium.d/run.d/xmsim\_restart.env (deflated 30%)  
 adding: xcelium.d/run.d/hdlrun.var (deflated 72%)  
 adding: xcelium.d/run.d/xmvlog.files (deflated 22%)  
 adding: xcelium.d/run.d/xmelab.hrd (deflated 6%)  
 adding: xcelium.d/run.d/.incl2.file (deflated 71%)  
 adding: xcelium.d/run.d/xmelab.env (deflated 40%)  
 adding: xcelium.d/run.d/6427a52e0b86\_13\_cdsrun.lib (deflated 6%)  
 adding: xcelium.d/run.d/cdsrun.lib (deflated 6%)  
 adding: xcelium.d/run.d/6427a52e0b86\_13\_hdlrun.var (deflated 72%)  
 adding: xcelium.d/run.d/.xmlib.lock (stored 0%)  
 adding: xcelium.d/run.d/.xmlibpath (deflated 28%)  
 adding: xcelium.d/run.d/hdl.var (deflated 42%)  
 adding: xcelium.d/run.d/files.ts (deflated 30%)  
 adding: xcelium.d/run.d/xmsim.args (deflated 31%)  
 adding: xrun.log (deflated 52%)  
 adding: xrun.history (deflated 6%)  
 adding: .simvision/ (stored 0%)  
 adding: .simvision/passwd (deflated 31%)  
 adding: ufsm.v (deflated 67%)  
 adding: parityGS.v (deflated 55%)  
 adding: rfifo.sv (deflated 64%)  
 adding: tsr\_tb.sv (deflated 53%)  
 adding: parity\_check.v (deflated 56%)  
 adding: baud\_gen.v (deflated 54%)  
 adding: uart\_top\_tb.sv (stored 0%)  
 adding: receiver\_tb.sv (deflated 64%)  
 adding: mux.v (deflated 54%)  
 adding: rfsm.v (deflated 70%)  
 adding: baud\_gen\_tb.sv (deflated 48%)  
 adding: dump.tcl (deflated 20%)  
 adding: dmux.v (deflated 44%)  
 adding: rsr\_tb.sv (deflated 57%)  
 adding: mux\_2\_1.v (deflated 39%)  
 adding: transmitter.v (deflated 56%)  
 adding: fifo\_tb.sv (deflated 65%)  
 adding: receiver.v (deflated 63%)  
 adding: parity\_gen\_tb.sv (deflated 51%)  
 adding: rfsm\_tb.sv (deflated 54%)  
 adding: rsr.sv (deflated 56%)  
 adding: work/ (stored 0%)  
 adding: tsr.sv (deflated 53%)  
 adding: testbench.sv (deflated 78%)  
 adding: design.sv (deflated 66%)  
 adding: mux\_2\_1\_tb.sv (deflated 42%)  
 adding: transmitter\_tb.sv (deflated 61%)  
 adding: parity\_gen.v (deflated 25%)  
 adding: tfifo.sv (deflated 65%)  
 adding: ufsm\_tb.v (deflated 50%)  
 adding: ACCELLERA\_OVL\_VHDL\_LIB/ (stored 0%)  
 adding: .bash\_logout (stored 0%)  
 adding: .bashrc (deflated 23%)  
 adding: .bash\_profile (deflated 21%)

